



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Isamu Kobori et al. Art Unit : Unknown
Serial No. : 10/623,581 Examiner : Unknown
Filed : July 22, 2003
Title : METHOD OF MANUFACTURING A SEMICONDUCTOR METHOD OF
MANUFACTURING A THIN-FILM TRANSISTOR AND THIN-FILM
TRANSISTOR

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

ASSOCIATE POWER OF ATTORNEY

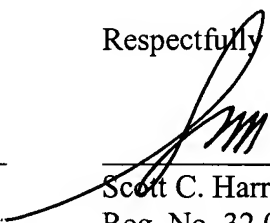
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Please direct all future official communications to Customer No. 26171.

Respectfully submitted,

Date: _____

9/17/03



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